

VISA-054



September 12, 2001

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To: Commissioner of Patents and Trademarks
Washington, D.C. 20231

Fr: George O. Saile, Reg. No. 19,572
20 McIntosh Drive
Poughkeepsie, N.Y. 12603

Subject:

Serial No. 09/896,486 06/29/01

Sunetra K. Mendis, Tzi-Hsiung Shu

AN ON-CHIP DESIGN-FOR-TESTING
STRUCTURE FOR CMOS APS (ACTIVE
PIXEL SENSOR) IMAGE SENSOR

Grp. Art Unit: 2877

INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation
In An Application.

The following Patents and/or Publications are submitted to
comply with the duty of disclosure under CFR 1.97-1.99 and
37 CFR 1.56. Copies of each document is included herewith.

U.S. Patent 5,841,126 to Fossum et al., "CMOS Active Pixel
Sensor Type Imaging System on a Chip", describes a CMOS active
pixel sensor (APS) type imaging system on a chip.

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U.S. Patent 5,990,506 to Fossum et al., "Active Pixel Sensors with Substantially Planarized Color Filtering Elements," describes a semiconductor imaging system preferably having an active pixel sensor array compatible with a CMOS fabrication process.

U.S. Patent 5,471,515 to Fossum et al., "Active Pixel Sensor with Intra-Pixel Charge Transfer," describes an imaging device formed as a monolithic complementary metal oxide semiconductor integrated circuit in an industry standard complementary metal oxide semiconductor process.

U.S. Patent 5,965,871 to Zhou et al., "Column Readout Multiplexer for CMOS Image Sensors with Multiple Readout and Fixed Pattern Noise Cancellation," describes an active pixel sensing structure including an array of pixel unit cells each of which are adapted to alternate between a light sensing mode wherein the cell outputs an image signal and a reset mode wherein the cell outputs a reset signal.

U.S. Patent 5,900,623 to Tsang et al., "Active Pixel Sensor Using CMOS Technology with Reverse Biased Photodiodes," describes an active pixel sensor implemented with CMOS technology that employs an array of photocells.

Sincerely,



Stephen B. Ackerman, Reg. #37761

